

Features

- Output Current of 1.5A
- Output transistor safe area protection
- No external components
- Package: SOT223

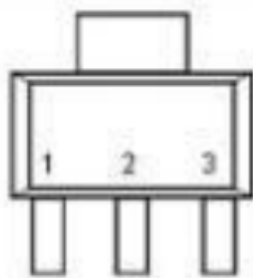
General Description

LM7805MPX is three-terminal positive regulators. One of these regulators can deliver up to 1.5A of output current. When used as a replacement

Zener diode-resistor Combination, an effective improvement in output impedance can be obtained, together with lower quiescent current.

Pin Configuration

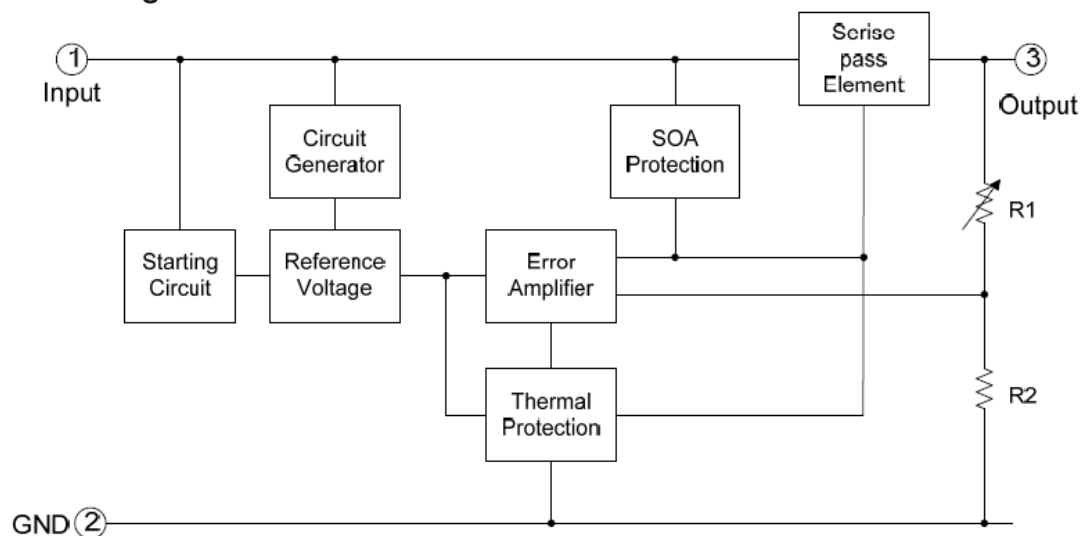
SOT223 (Top View)



SOT-223

PIN NO.	PIN NAME	FUNCTION
1	VIN	Input voltage pin
2	GND	Ground pin
3	VOUT	Output voltage pin

Block Diagram



Absolute Maximum Ratings (Ta=25°C)

Parameter	Rating	Unit
Input supply voltage: VIN	35	V
MAX. Output current:Iout	1500	mA
MAX Power:Pmax	1	W
Maximum junction temperature:Tj	-40~125	°C
Storage temperature:Tstr	-55~155	°C
Soldering temperature and time	+260(Recommended 10S)	°C

Note: The absolute maximum ratings are rated values exceeding which the product could suffer physical damage. These values must therefore not be exceeded under any conditions.

LM7805MPX

Electrical Characteristics

(C_{in}=0.33μF, C_o=0.1μF, T_a=25°C, unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Output Voltage	V _{out}	I _o =40mA, V _{IN} =10V	0.964v _{out}	v _{out}	1.036v _{out}	V
		I _o =1mA~40mA V _{IN} =7V~18V	0.96v _{out}	v _{out}	1.04v _{out}	
		I _o =10mA V _{IN} =10V	0.95v _{out}	v _{out}	1.05v _{out}	
Line Regulation	LNR	V _{IN} =7V~18V, I _o =40mA	-150	-	150	mV
		V _{IN} =8V~18V, I _o =40mA	-100	-	100	
Load Regulation	LDR	V _{IN} =10V, I _o =1mA~100mA	-60	-	60	mV
		V _{IN} =10V, I _o =1mA~40mA	-30	-	30	
Dropout Voltage	V _{DIF}	T _a =25°C, I _o =500mA	-	1.7	-	V
Quiescent Current	I _Q	V _{IN} =10V	-	1.5	--	mA
Quiescent Current Change	ΔI _Q	V _{IN} =8V~18V, I _o =40mA	-1.5	-	1.5	mA
		V _{IN} =10V, I _{OUT} =1mA~40mA,	-0.1	-	0.1	

LNR: Line Regulation. The change in output voltage for a change in the input voltage. The measurement is made under conditions of low dissipation or by using pulse techniques such that the average chip temperature is not significantly affected.

LDR: Load Regulation. The change in output voltage for a change in load current at constant chip temperature.

Typical Performance Characteristics

